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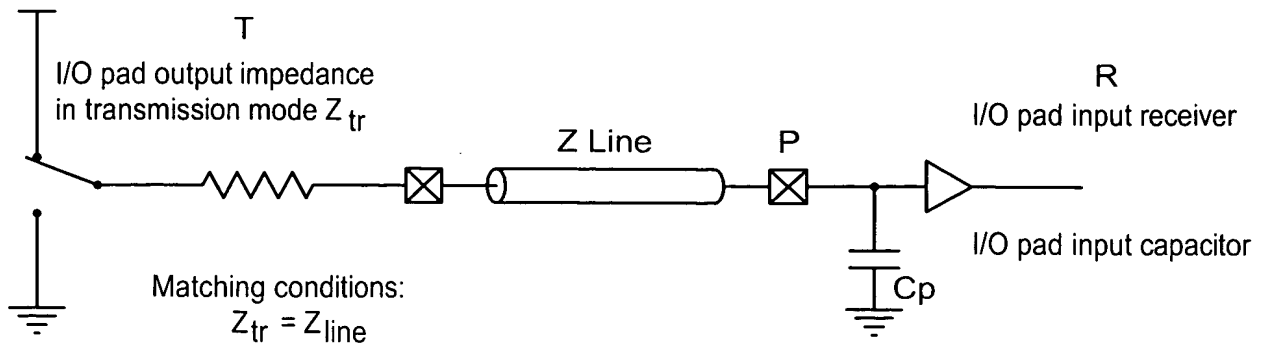


Fig. 1A

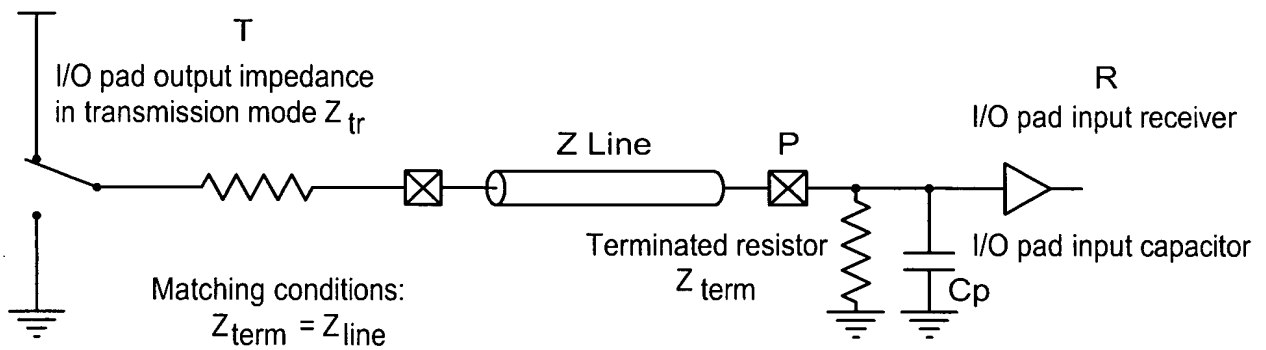


Fig. 1B

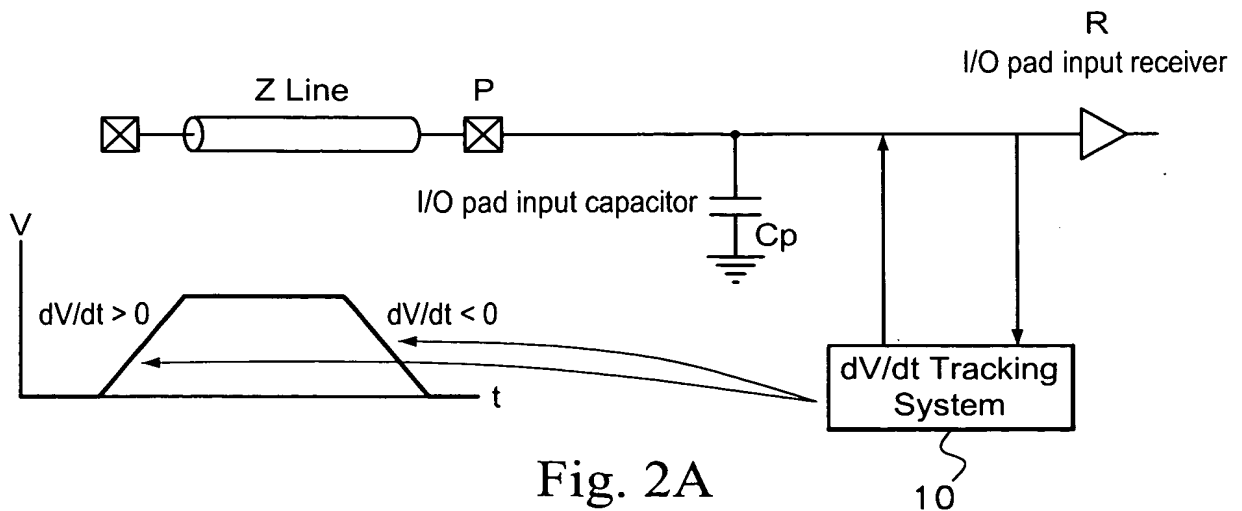


Fig. 2A

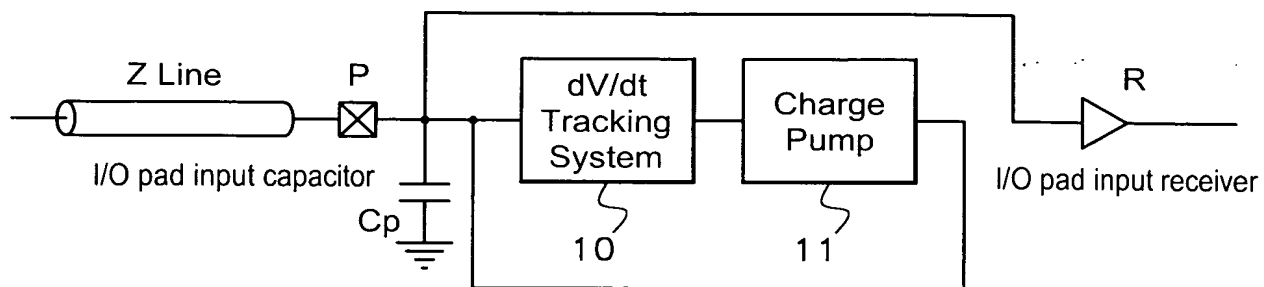


Fig. 2B

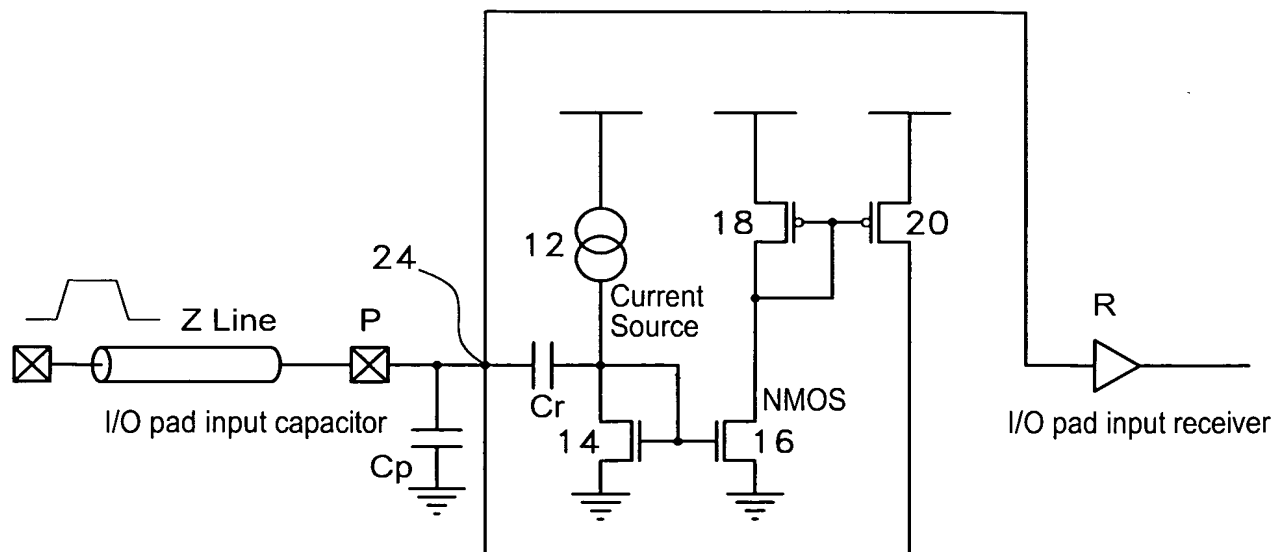
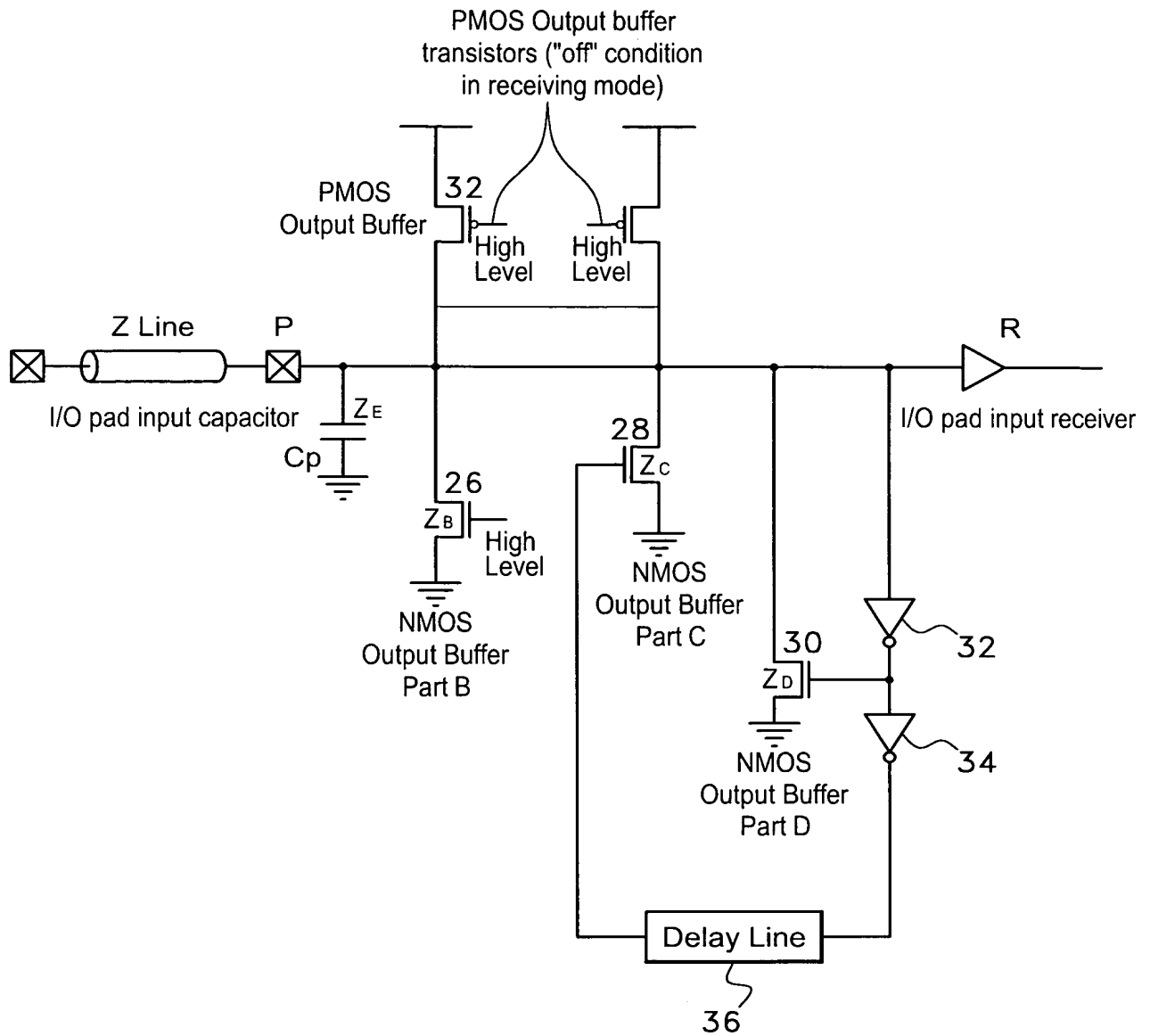


Fig. 2C



$Z_{input} = Z_B \parallel [Z_C \text{ (or } Z_D, \text{ or } Z_E)]$
 if $|Z_C| = |Z_D| = |Z_E|$, then an Input signal can not change Z input

Fig. 2D

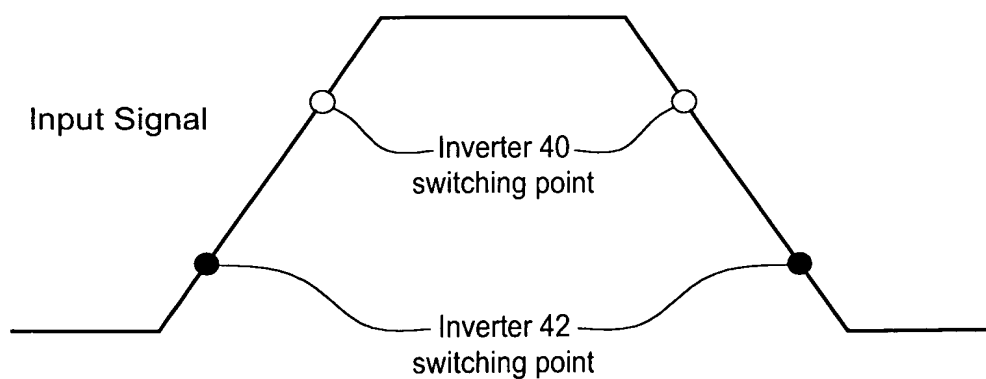
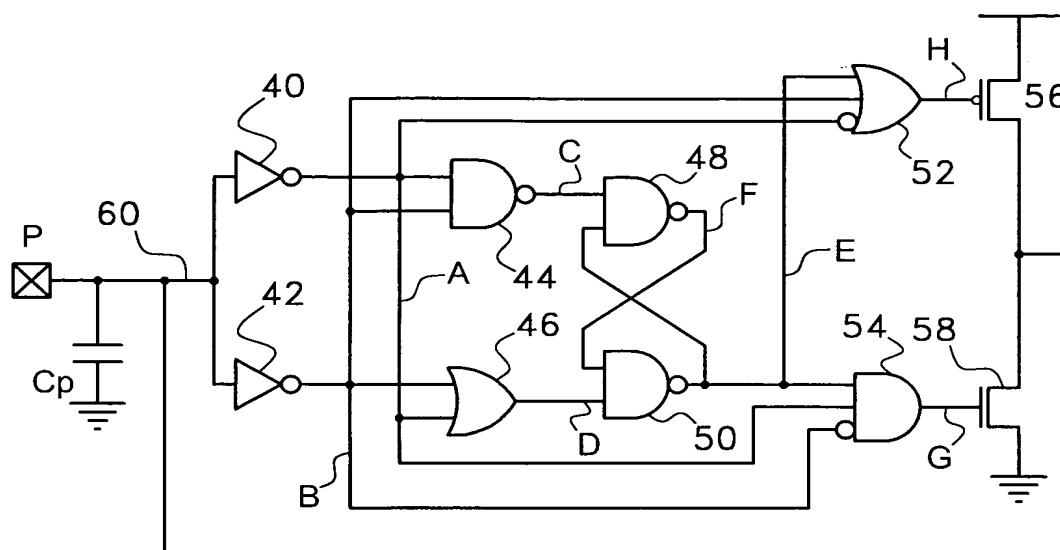


Fig. 3

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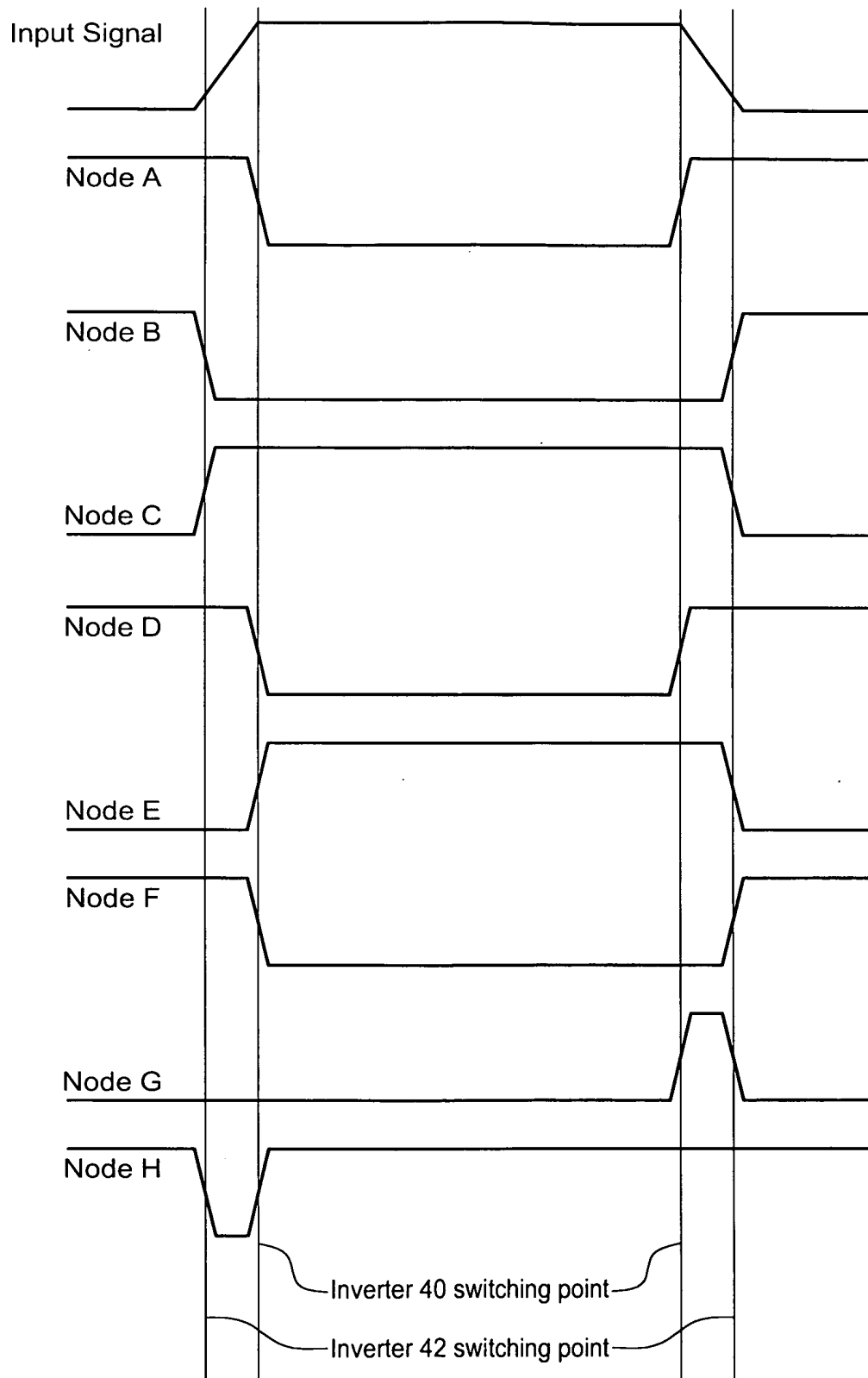


Fig. 4